

Võrdluskem *Magnitude Comparator*

a ₀	=	G
a ₁		L
b ₀		
b ₁		E

a₁

a₀

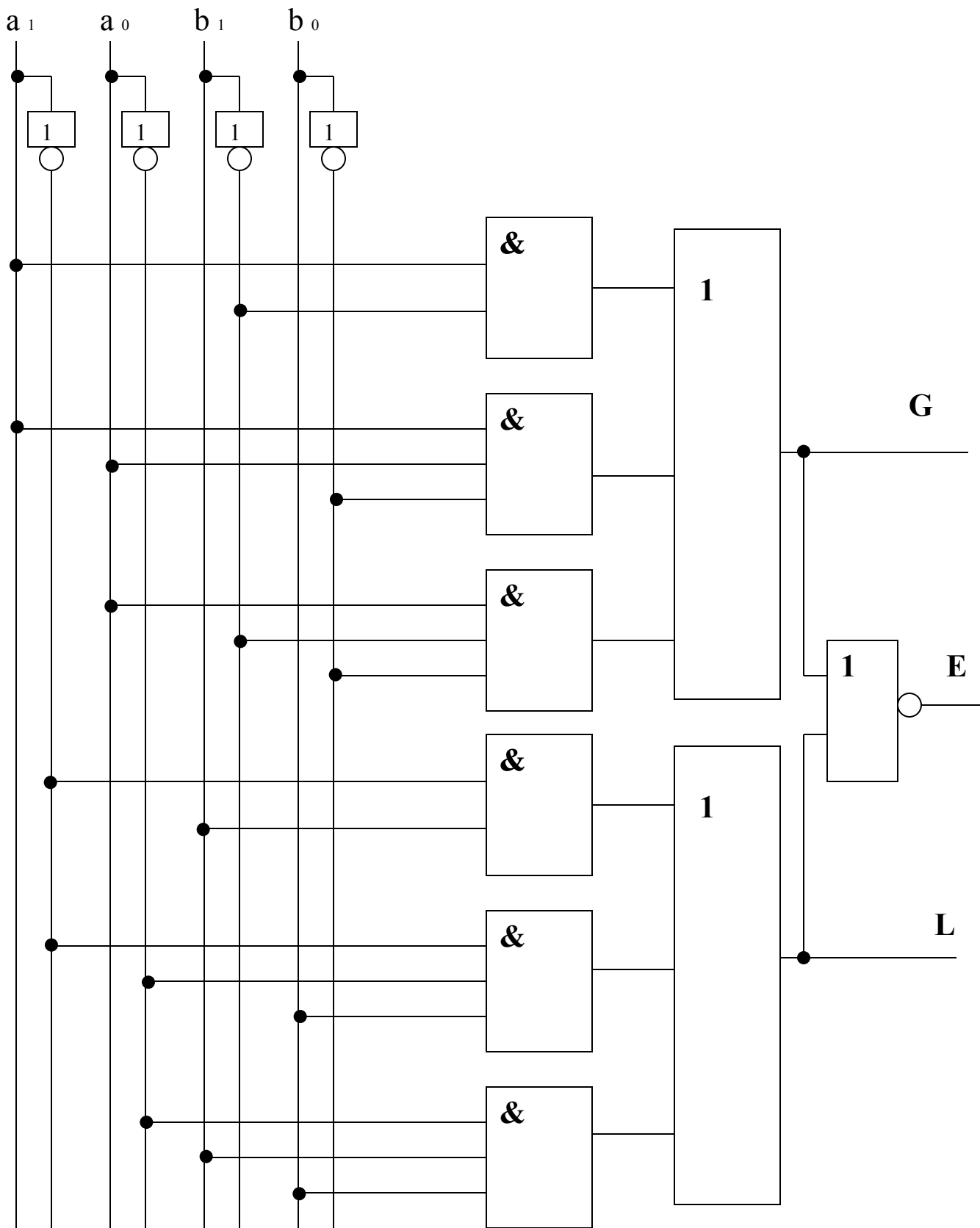
b₁

b₀

G

L

0	0	0	0		0	0
0	0	0	1		0	1
0	0	1	0		0	1
0	0	1	1		0	1
0	1	0	0		1	0
0	1	0	1		0	0
0	1	1	0		0	1
0	1	1	1		0	1
1	0	0	0		1	0
1	0	0	1		1	0
1	0	1	0		0	0
1	0	1	1		0	1
1	1	0	0		1	0
1	1	0	1		1	0
1	1	1	0		1	0
1	1	1	1		0	0



10/08/202

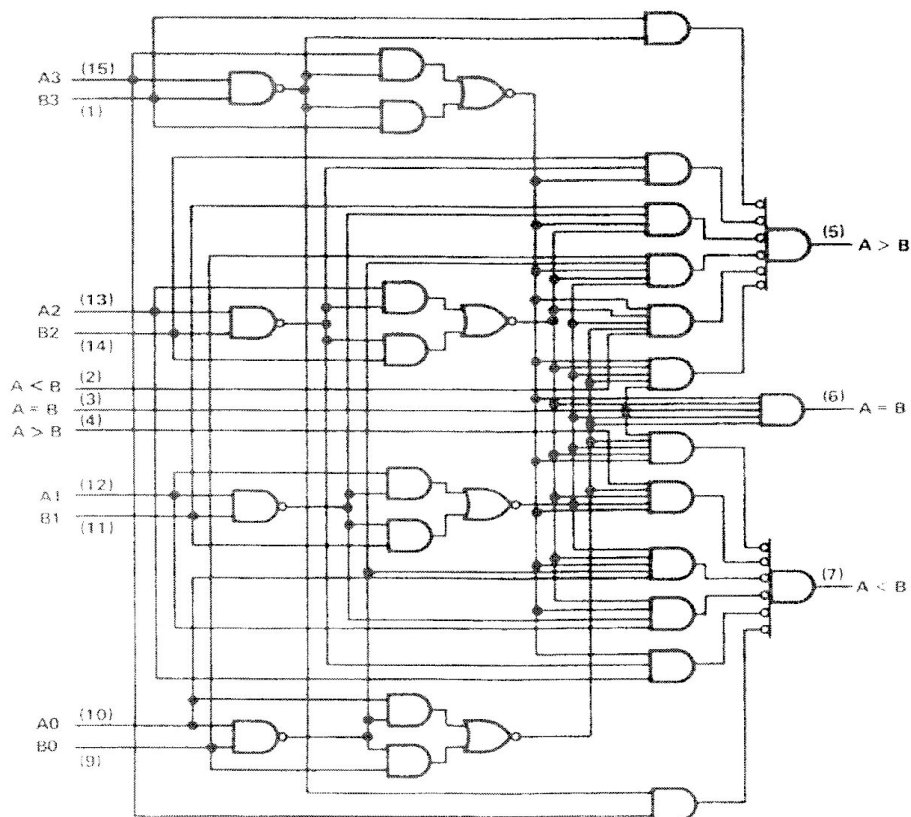
T. Evartson

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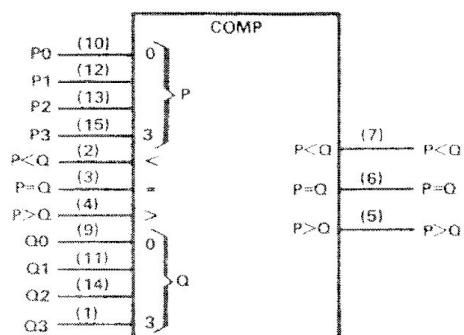
SN5485, SN54LS85, SN54S85
SN7485, SN74LS85, SN74S85
4-BIT MAGNITUDE COMPARATORS

SDLS123 - MARCH 1974 - REVISED MARCH 1988

logic diagrams (positive logic)



logic symbol†



†This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.
Pin numbers shown are for D, J, N, and W packages.

 **TEXAS
INSTRUMENTS**

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